1/12

Title:

Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

Inventor(s): Gir

Girish R. Kamath

Appln. No. Not yet assigned Docket # 37125/31374

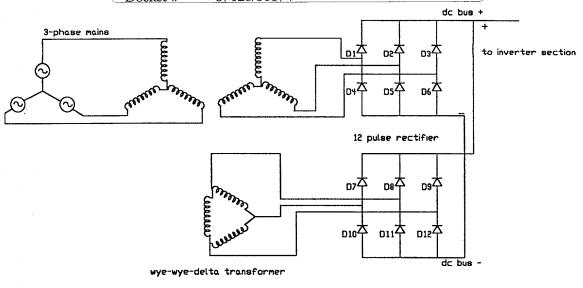


Fig. 1 Prior Art

Title:

Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

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Girish R. Kamath

Appln. No.

Not yet assigned

Docket #

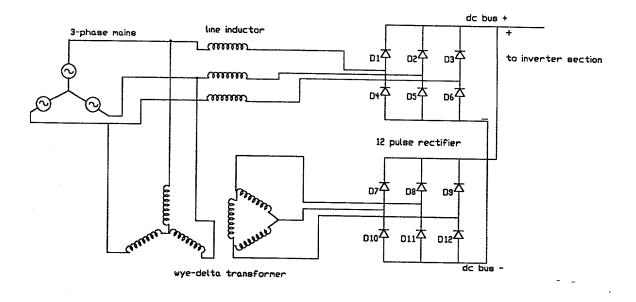


Fig. 2 Prior Art

Title:

Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

Inventor(s): Appln. No.

Girish R. Kamath

Appln. No. Not yet assigned Docket # 37125/31374

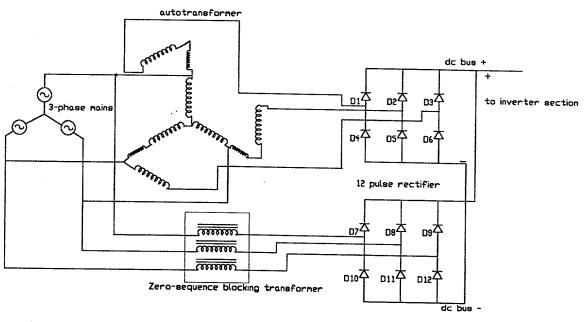


Fig. 3 Prior Art

Title:

Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

Inventor(s):

Girish R. Kamath Not yet assigned

Appln. No. Docket #

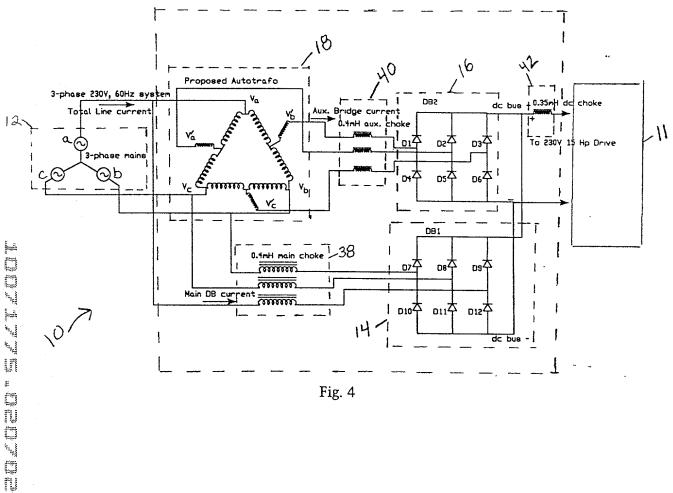
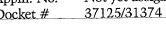


Fig. 4

of Current Harmonics Reduction in a Circuit

Inventor(s): Appln. No. Docket # Girish R. Kamath Not yet assigned



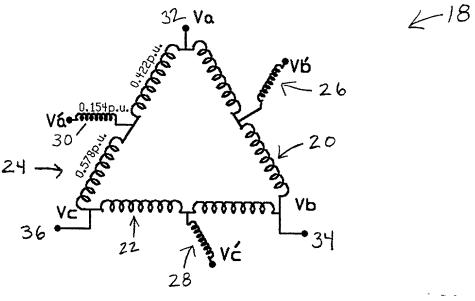


Fig. 5

6 / 12 Title: Autotransformer-F

Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

Inventor(s): Appln. No. Docket #

Girish R. Kamath Not yet assigned 37125/31374

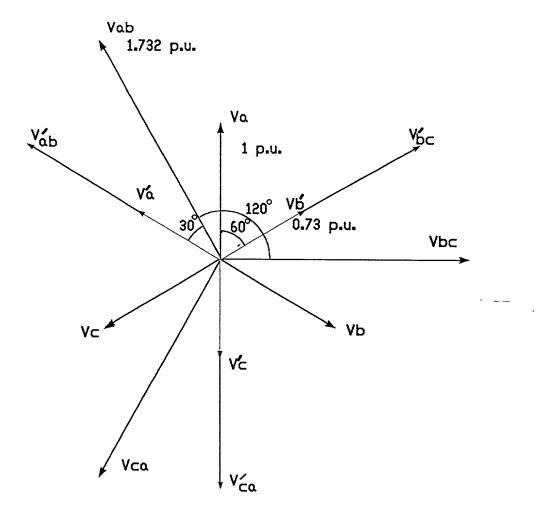


Fig. 6

Title:

 $7\,/\,12$  Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

Inventor(s): Appln. No.

Girish R. Kamath Not yet assigned

Docket #

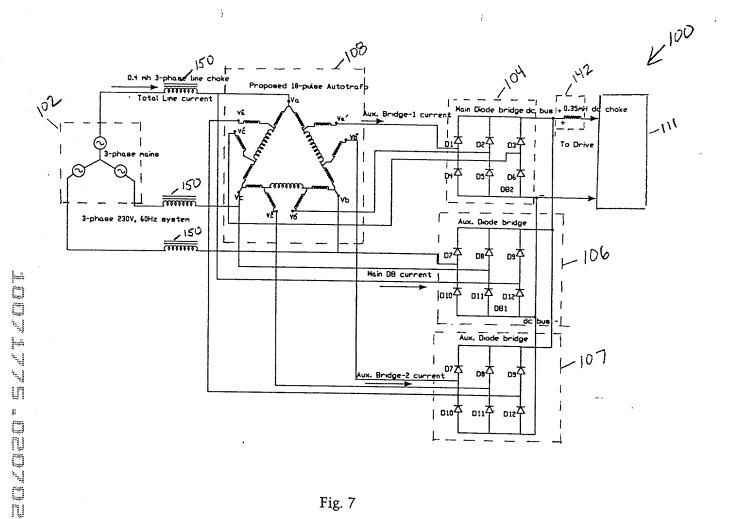


Fig. 7

Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

Inventor(s): Girish R. Kamath Appln. No. Not yet assigned Docket # 37125/31374

Title:

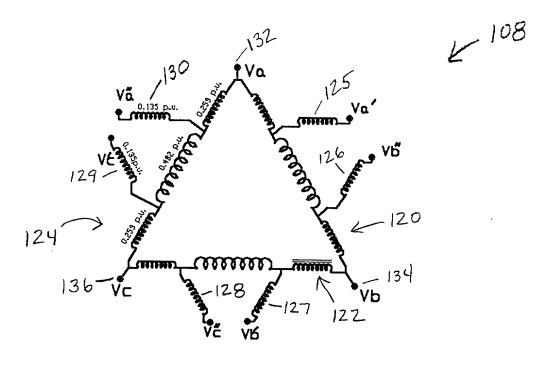


Fig. 8

Autotransformer-Based System and Method

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of Current Harmonics Reduction in a Circuit

Inventor(s):
Appln. No.
Docket #

Title:

Girish R. Kamath Not yet assigned 37125/31374

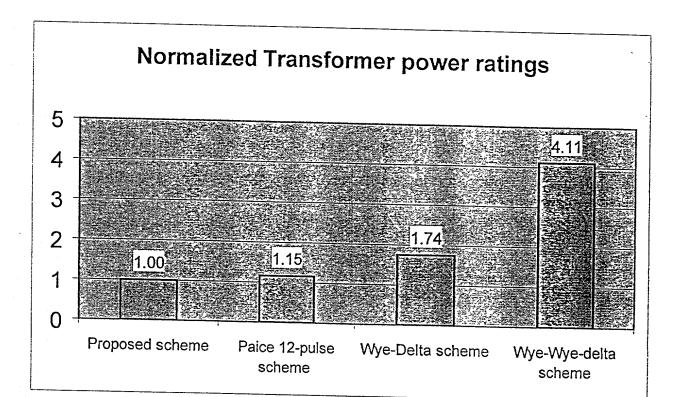


Fig. 9

Title:

 $10\,/\,12$  Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

Inventor(s): Appln. No.

Girish R. Kamath Not yet assigned

Docket #

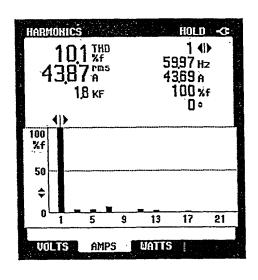


Fig. 10

Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit

Inventor(s): Appln. No.

Docket #

Title:

Girish R. Kamath Not yet assigned 37125/31374

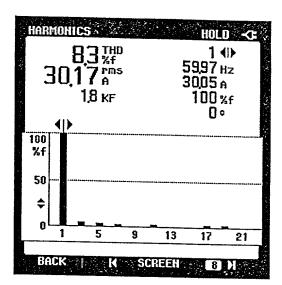


Fig. 11A

Title: Autotransformer-Based System and Method

of Current Harmonics Reduction in a Circuit

Inventor(s): Girish R. Kamath Appln. No. Not yet assigned Docket # 37125/31374

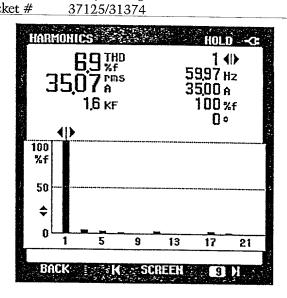


Fig. 11B